

Dr. Mona Mathur

Dr. Mona MATHUR has an extremely rich cross disciplinary exposure with a balanced industrial and academic experience totaling 18 fulfilling and technically challenging years. She approaches every problem with an application end in mind and is a VLSI designer with an instrumentation and control background, who later successfully developed a computational model for development of neuronal circuitry for human vision as a part of her PhD thesis.

She now heads a joint initiative between the Indian Institute of Technology, Delhi and ST Microelectronics's Advanced Systems Technology group which focuses on advanced research in the areas of Multimedia Protocols and Platforms. She is an adjunct faculty at IIT Delhi and her research interests include image processing, pattern recognition, multimedia systems, embedded systems, and advanced video coding algorithms and architectures. She has several publications in national and international journals and conferences, and is a regular speaker at several industry events. She was awarded the SK Mitra Memorial award in 2005 for the best research oriented paper. Mona Mathur is a B.E. (Instrumentation and Control) from D.I.T., Delhi, and completed her M.Tech and Ph.D. degrees at the Department of Electrical Engineering, I.I.T. Delhi.